

J1033 U.S. PTO  
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Substitute for form 1449

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

Application No. TBA  
Applicant: Gunzinger  
Filing Date: Herewith  
Title: Method of Rapid Communication  
within a Parallel Computer System  
and a Parallel Computer System  
Operated by the Method  
Attorney Docket No.: FREI.P-033US-2

Page 1 of 1

**U.S. PATENT DOCUMENTS**

Examiners Initials	U S Patent No.	Name of Persons or applicant	Date of Publication of Cited Document
RLÉ	5,117,350	Parrish et al.	May 26, 1992
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RLÉ	WO 91/10200	Maspar Computer Corporation	July 11, 1991
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**OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

Examiner Initials	
RLÉ	Keeping It Simple, by Tom R. Halfhill, Byte, October 1997, pages 51 - 52
RLÉ	Applications Considerations in the System Design of Highly Concurrent Multiprocessors, by Stephen F. Lundstrom, pp. 1292 - 1309, 8092 IEEE Transactions on Computers, C-36 (1987), No. 11, New York, NY

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Richard Elik  
Examiner Signature

6/27/2002  
Date Considered

Substitute for form 1449

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**



Application No. 09/954,596

Applicant: Gunzinger

Filing Date: September 12, 2001

Title: Method of Rapid Communication  
within a Parallel Computer System  
and a Parallel Computer System  
Operated by the Method

Attorney Docket No.: FREI.P-033US-2

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**FOREIGN PATENT DOCUMENTS**

Examiners Initials	Patent No.	Name of Persons or applicant	Date of Publication of Cited Document
X			

Richard Ellis

6/27/2002

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**



Application No. 09/954,596

Applicant: Gunzinger

Filing Date: September 12, 2001

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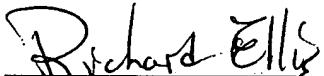
Attorney Docket No.: FREI.P-033US-2

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<i>RVE</i>	Motlagh, B.S. et al., <i>Memory latency in distributed shared-memory multiprocessors</i> , Proceedings IEEE Southeastcon '98, pp. 24-26, April 1998.
<i>RVE</i>	Kuskin, J. et al., <i>The Stanford FLASH multiprocessor</i> , Proceedings of the 24 <sup>th</sup> Annual International Symposium on Computer Architecture, IEEE, pp 18-21, April 1994
<i>RVE</i>	Prete, C.A. et al., <i>Reducing coherence-related overhead in multiprocessor systems</i> , Proceedings of Euromicro Workshop on Parallel and Distributed Processing, pp 25-27, January 1995.

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Examiner Signature

*6/27/2002*  
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Date Considered